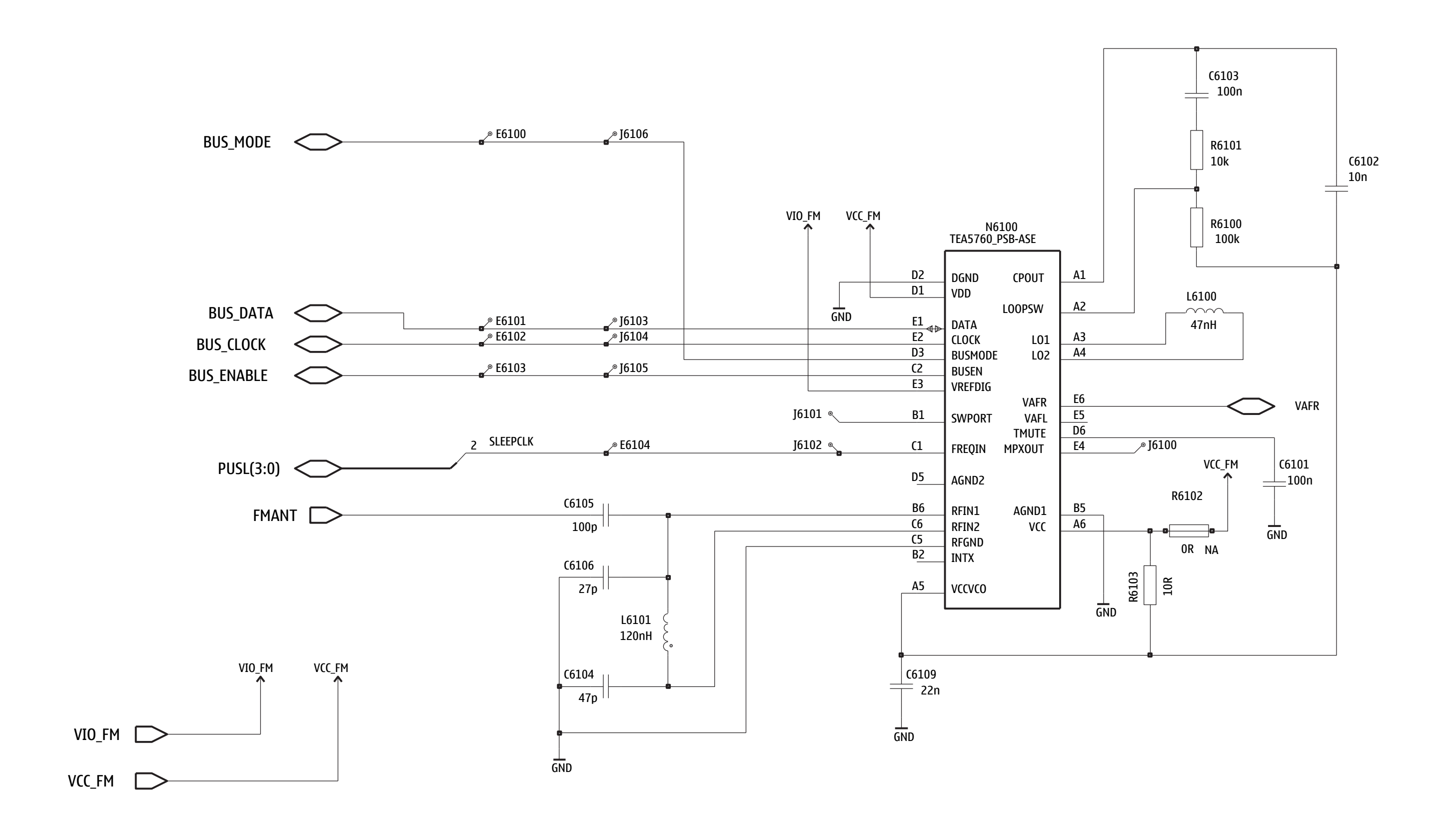
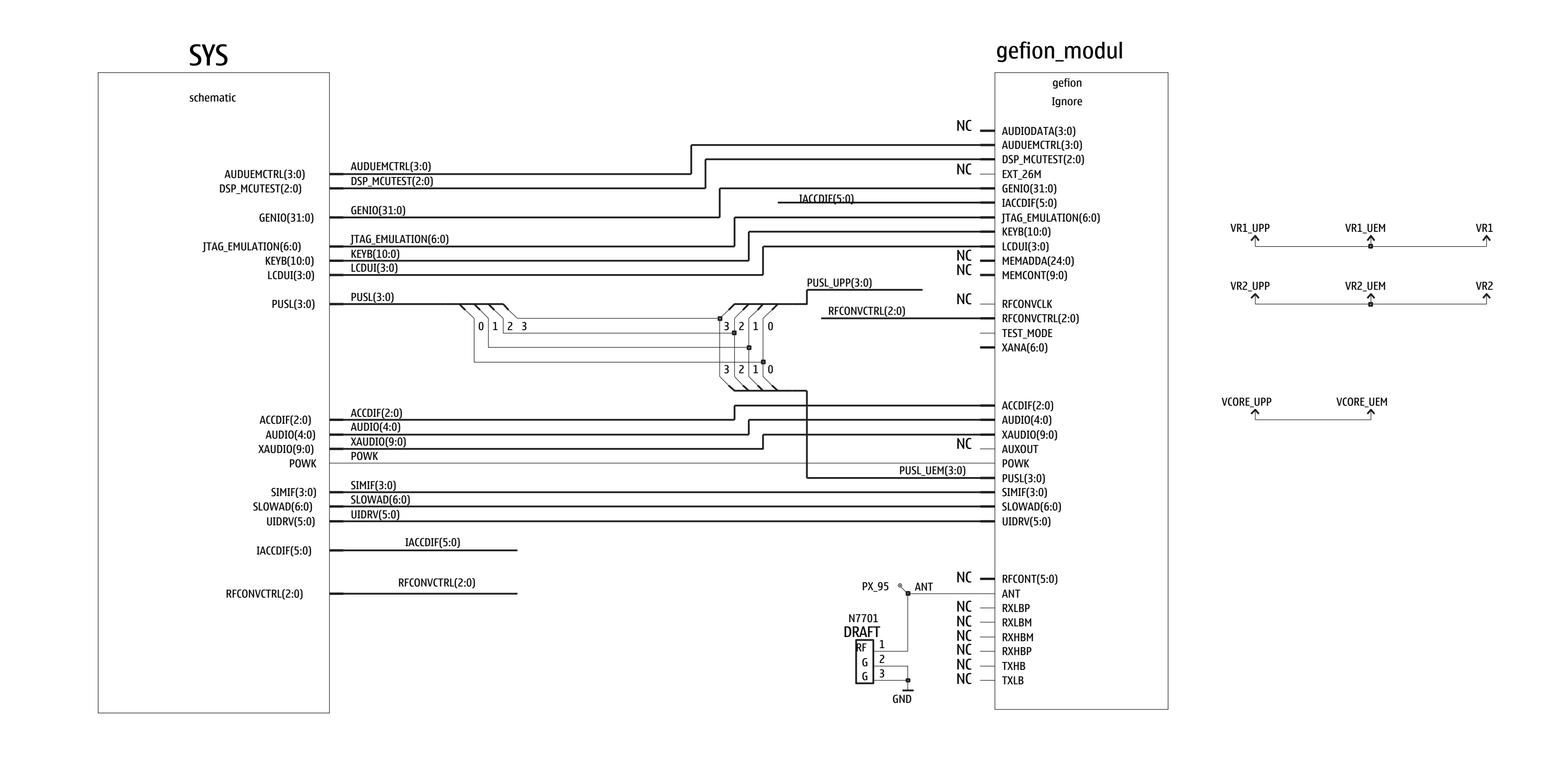
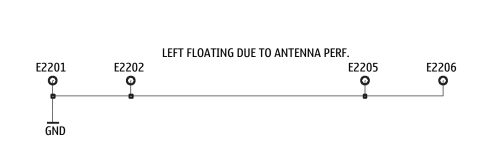
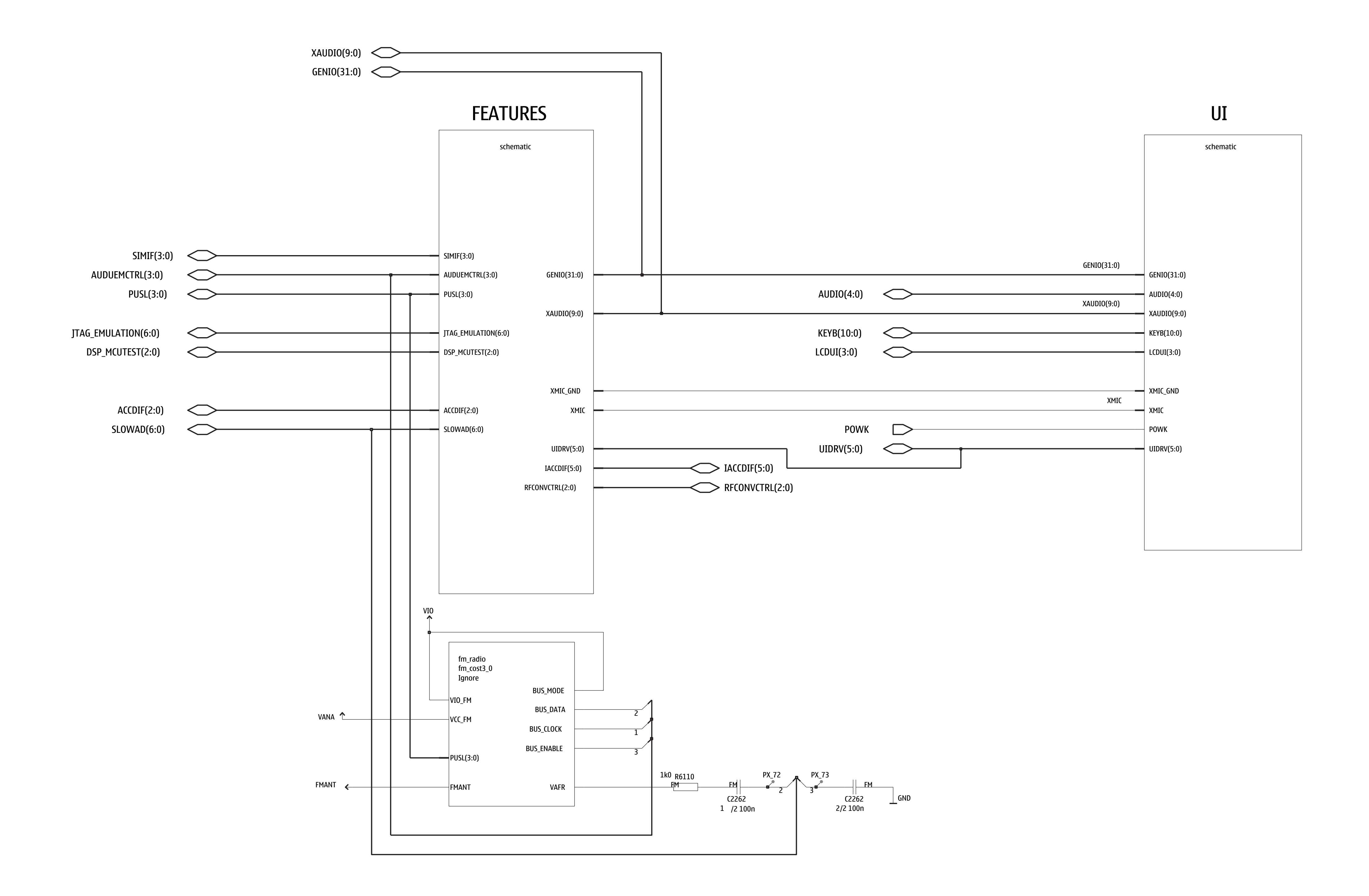
FM COST 3.0 A1.0 2006 WK02 RELEASE

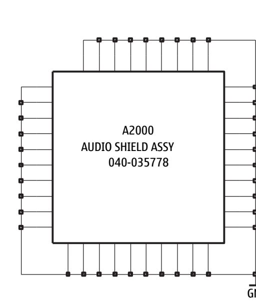


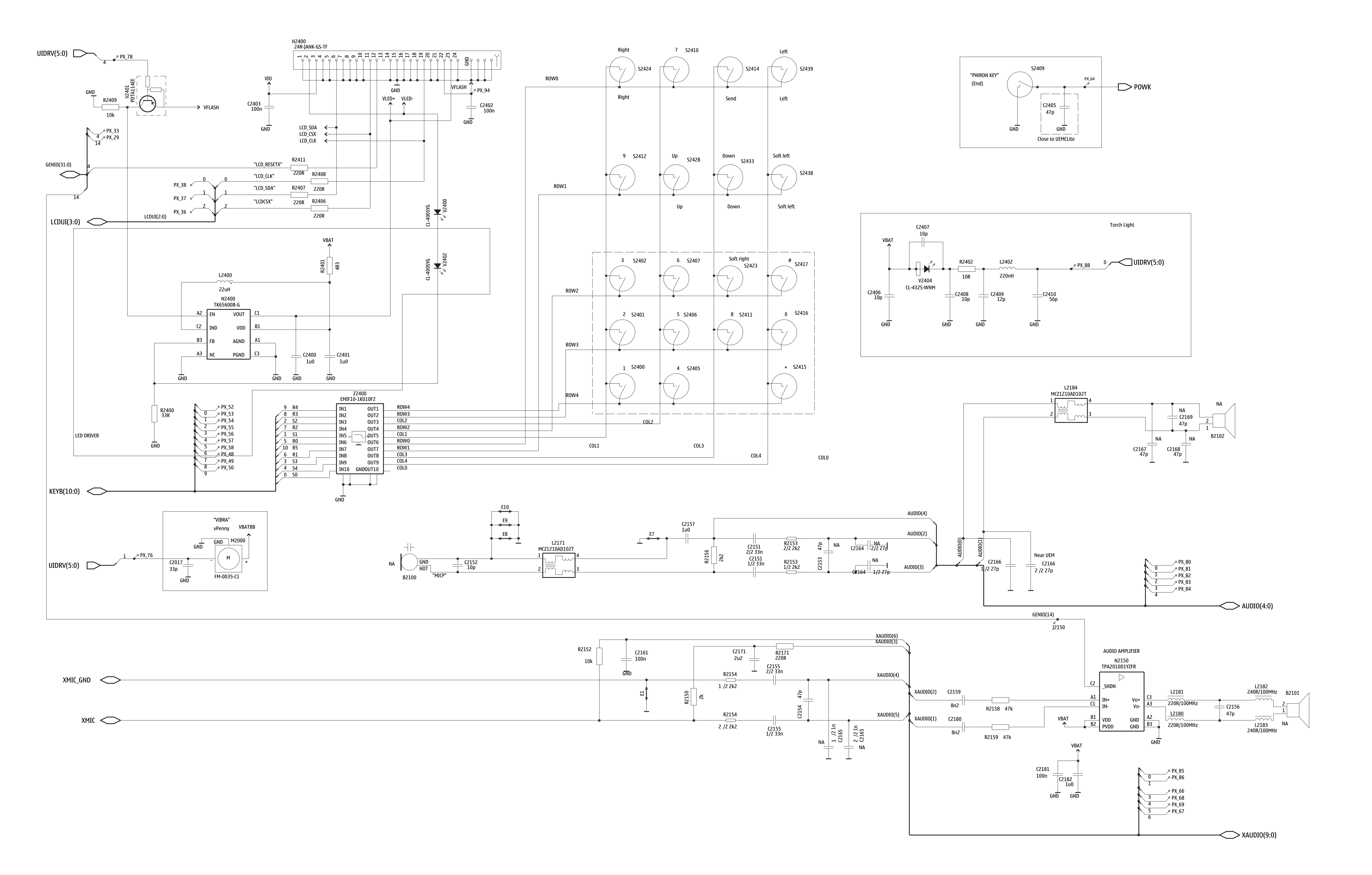
REF RANGE = 6100-6199


SYS
schematic
AUDUEMCTRL(3:0)
DSP_MCUTEST(2:0)
GENIO(31:0)
JTAG_EMULATION(6:0)
KEYB(10:0)
LCDUI(3:0)
PUSL(3:0)
ACCDIF(2:0)
AUDIO(4:0)
XAUDIO(9:0)
POWK
SIMIF(3:0)
SLOWAD(6:0)
UIDRV(5:0)
IACCDIF(5:0)
RFCONVCTRL(2:0)
PUSL_UPP(3:0)
PUSL_UEM(3:0)
0 1 2 3
3 2 1 0
3 2 1 0
gefion_modul
gefion
Ignore
NC
AUDIODATA(3:0)
AUDUEMCTRL(3:0)
DSP_MCUTEST(2:0)
EXT_26M
GENIO(31:0)
IACCDIF(5:0)
JTAG_EMULATION(6:0)
KEYB(10:0)
LCDUI(3:0)
MEMADDA(24:0)
MEMCONT(9:0)
RFCONVCLK
RFCONVCTRL(2:0)
TEST_MODE
XANA(6:0)
ACCDIF(2:0)
AUDIO(4:0)
XAUDIO(9:0)
AUXOUT
POWK
PUSL(3:0)
SIMIF(3:0)
SLOWAD(6:0)
UIDRV(5:0)
RFCONT(5:0)
ANT
RXLBP
RXLBM
RXHBM
RXHBP
TXHB
TXLB
PX_95
ANT
N7701
DRAFT
RF 1
G 2
G 3
GND
VR1_UPP
VR1_UEM
VR1
VR2_UPP
VR2_UEM
VR2
VCORE_UPP
VCORE_UEM

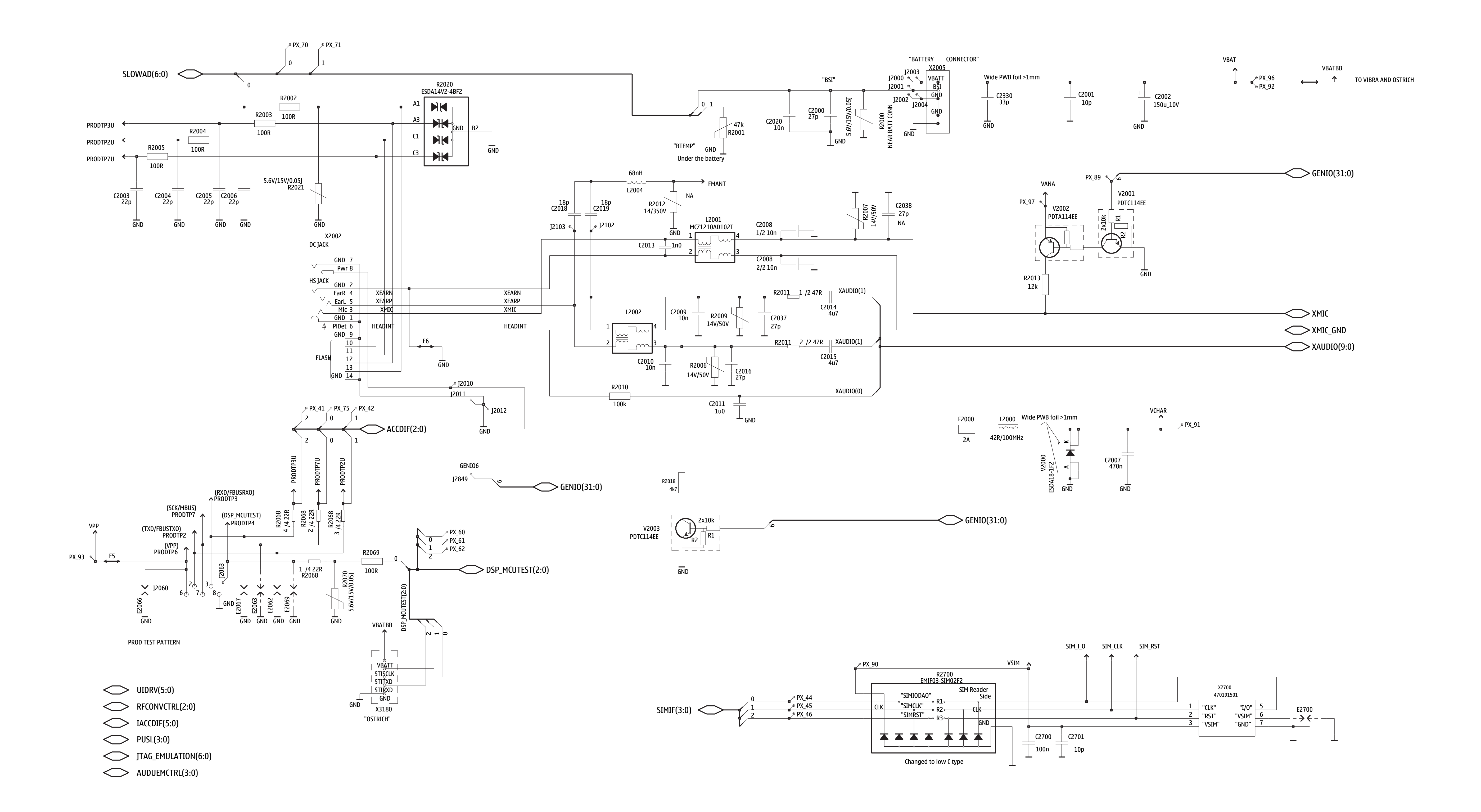

LEFT FLOATING DUE TO ANTENNA PERF.
E2201
E2202
E2205
E2206
GND


XAUDIO(9:0)
GENIO(31:0)
FEATURES
schematic
UI
schematic
SIMIF(3:0)
AUDUEMCTRL(3:0)
PUSL(3:0)
JTAG_EMULATION(6:0)
DSP_MCUTEST(2:0)
ACCDIF(2:0)
SLOWAD(6:0)
GENIO(31:0)
XAUDIO(9:0)
XMIC_GND
XMIC
UIDRV(5:0)
IACCDIF(5:0)
RFCONVCTRL(2:0)
AUDIO(4:0)
KEYB(10:0)
LCDUI(3:0)
POWK
UIDRV(5:0)
IACCDIF(5:0)
RFCONVCTRL(2:0)
VIO
fm_radio
fm_cost3_0
Ignore
BUS_MODE
BUS_DATA
BUS_CLOCK
BUS_ENABLE
VIO_FM
VCC_FM
PUSL(3:0)
FMANT
VAFR
VANA
FMANT
1k0
R6110
FM
C2262
1 /2 100n
PX_72
PX_73
C2262
2/2 100n
GND

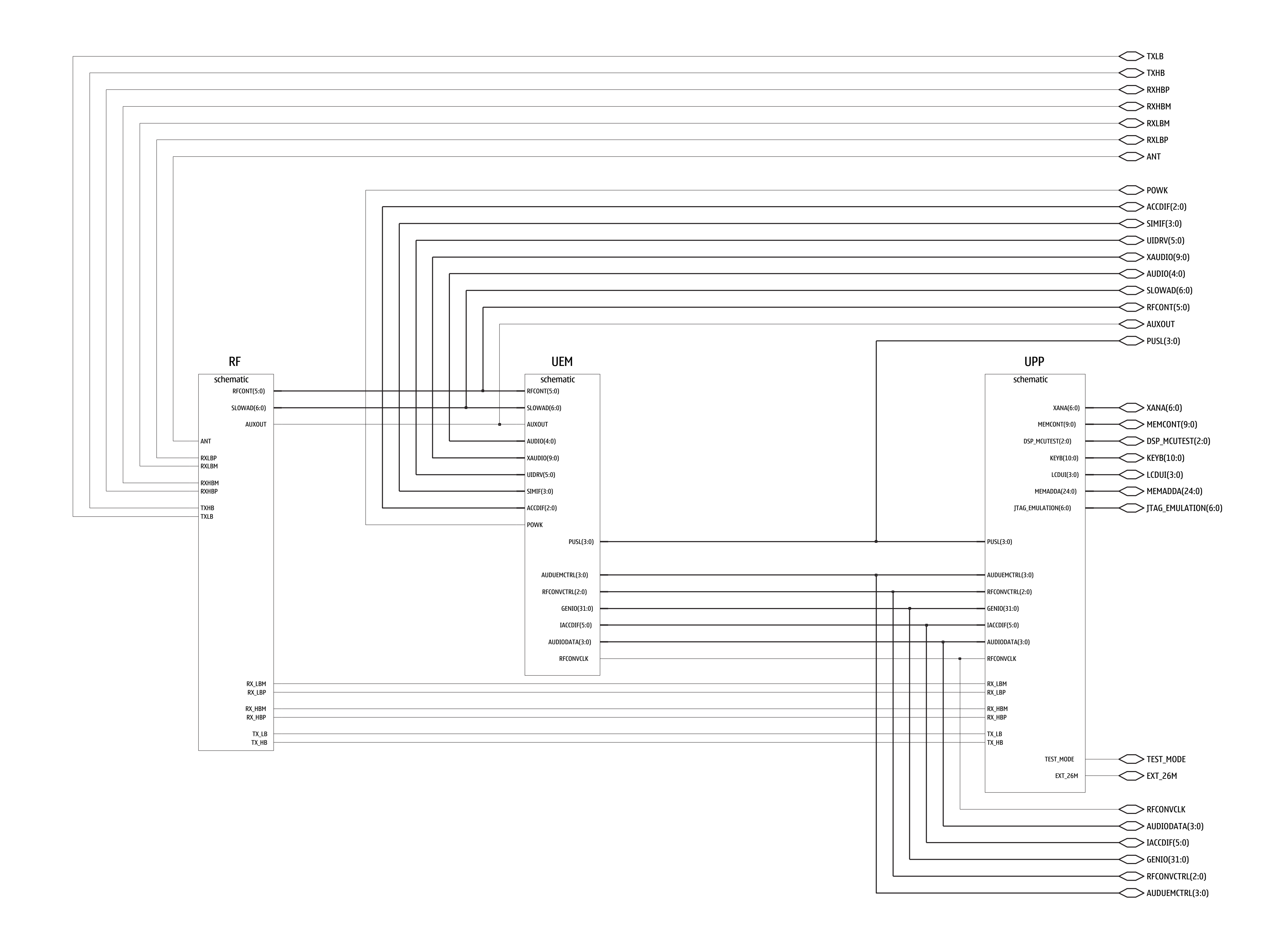

A2000
AUDIO SHIELD ASSY
040-035778
GND

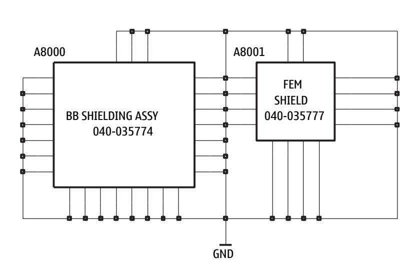


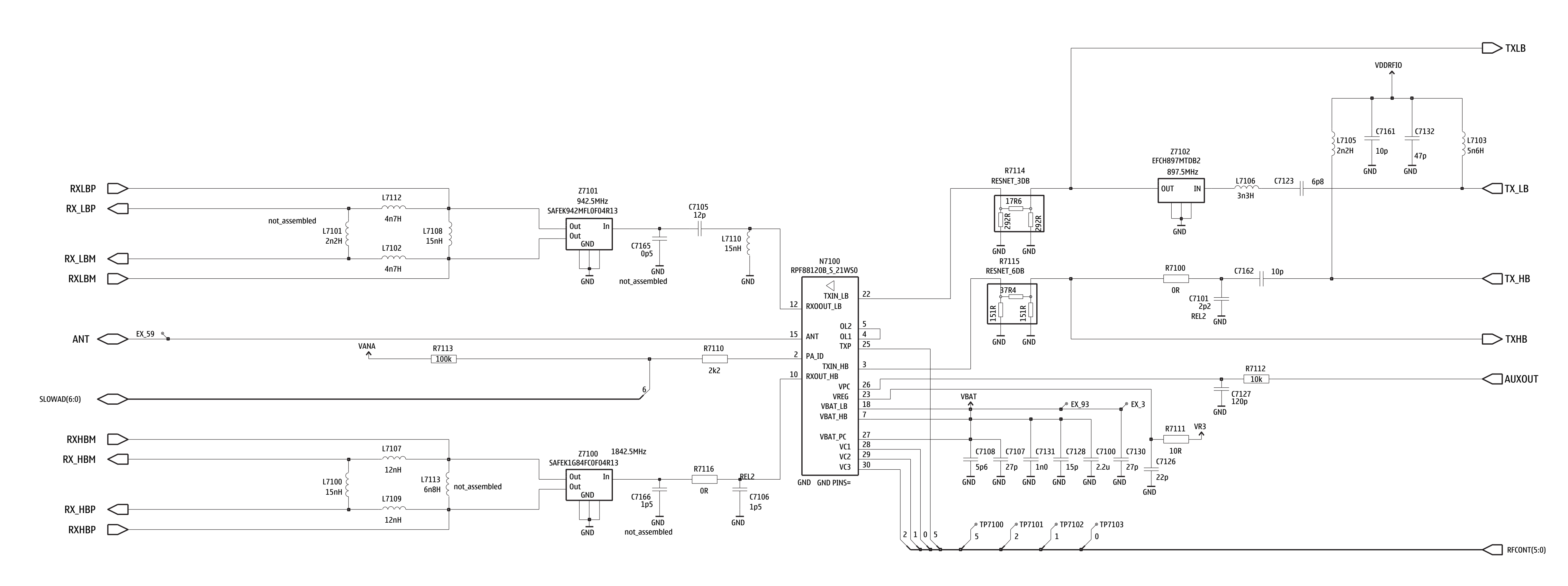
UIDRV(5:0)
H2400
24R-JANK-GS-TF
LCD_SDA
LCD_CSX
LCD_CLK
VFLASH
GENIO(31:0)
LCDUI(3:0)
LCDUI(2:0)
"LCD_RESETX"
"LCD_CLK"
"LCD_SDA"
"LCDCSX"
N2400
TK65600B-G
LED DRIVER
Z2400
EMIF10-1K010F2
KEYB(10:0)
"VIBRA"
vPenny
M2000
FM-0035-C1
ROW0
ROW1
ROW2
ROW3
ROW4
COL0
COL1
COL2
COL3
COL4
Right
Send
Left
Up
Down
Soft left
Soft right
"PWRON KEY"
(End)
Close to UEMCLite
POWK
Torch Light
CL-432S-WNM
CL-400SYG
L2184
MCZ1210AD102T
L2171
MCZ1210AD102T
"MICP"
Near UEM
AUDIO(4:0)
GENIO(14)
AUDIO AMPLIFIER
N2150
TPA2010D1YZFR
XMIC_GND
XMIC
XAUDIO(9:0)

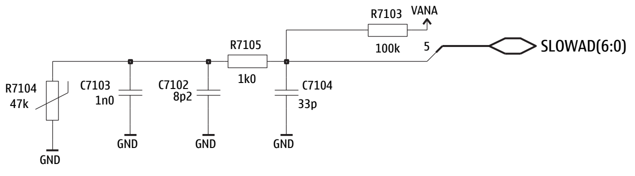


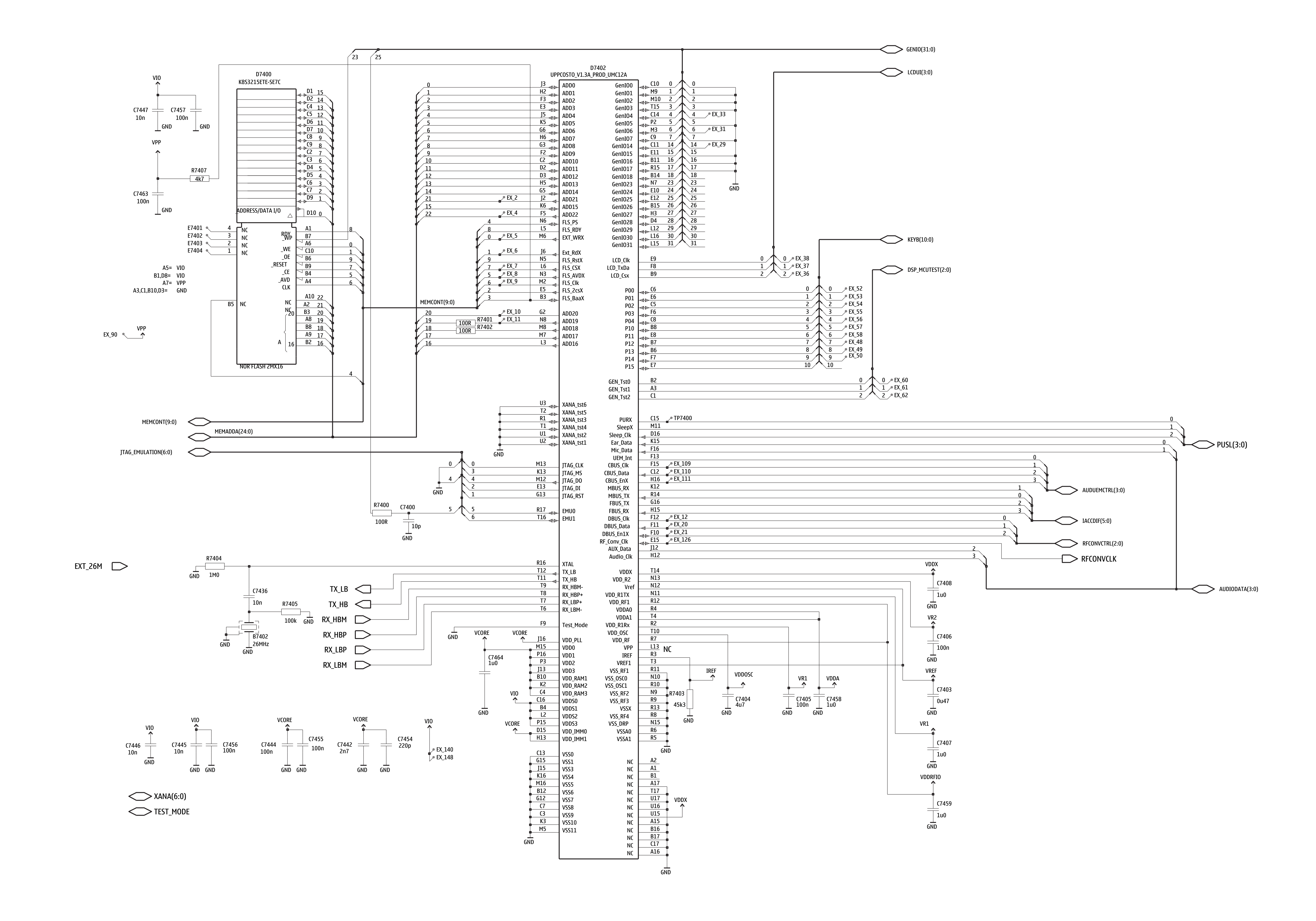
SLOWAD(6:0)
PX_70
PX_71
R2020
ESDA14V2-4BF2
R2002
100R
R2003
100R
R2004
100R
R2005
100R
PRODTP3U
PRODTP2U
PRODTP7U
C2003
22p
C2004
22p
C2005
22p
C2006
22p
GND
5.6V/15V/0.05J
R2021
X2002
DC JACK
HS JACK
FLASH
XEARN
XEARP
XMIC
HEADINT
E6
J2010
J2011
J2012
"BATTERY CONNECTOR"
X2005
VBATT
BSI
"BSI"
"BTEMP"
Under the battery
R2001
47k
C2020
10n
C2000
27p
R2000
NEAR BATT CONN
Wide PWB foil >1mm
C2330
33p
C2001
10p
C2002
150u_10V
VBAT
VBATBB
PX_96
PX_92
TO VIBRA AND OSTRICH
L2004
68nH
FMANT
R2012
14/350V
NA
C2018
18p
C2019
18p
J2103
J2102
L2001
MCZ1210AD102T
C2013
1n0
C2008
1/2 10n
2/2 10n
R2007
14V/50V
C2038
27p
L2002
C2009
10n
R2009
14V/50V
C2037
27p
R2011
C2014
4u7
XAUDIO(1)
C2010
10n
R2006
14V/50V
C2016
27p
C2015
4u7
R2010
100k
XAUDIO(0)
C2011
1u0
XMIC
XMIC_GND
XAUDIO(9:0)
GENIO(31:0)
PX_89
VANA
PX_97
V2002
PDTA114EE
V2001
PDTC114EE
2x10k
R2013
12k
F2000
2A
L2000
42R/100MHz
VCHAR
PX_91
V2000
ESDA18-1F2
C2007
470n
R2018
4k7
V2003
PDTC114EE
PX_41
PX_75
PX_42
ACCDIF(2:0)
GENIO6
J2849
PRODTP3U
PRODTP7U
PRODTP2U
(RXD/FBUSRXO)
PRODTP3
(SCK/MBUS)
PRODTP7
(DSP_MCUTEST)
PRODTP4
(TXD/FBUSTXO)
PRODTP2
(VPP)
PRODTP6
VPP
PX_93
E5
J2060
J2063
R2068
R2069
100R
R2070
5.6V/15V/0.05J
PX_60
PX_61
PX_62
DSP_MCUTEST(2:0)
E2066
E2067
E2063
E2062
E2069
PROD TEST PATTERN
VBATBB
STISCLK
STITXD
STIRXD
X3180
"OSTRICH"
UIDRV(5:0)
RFCONVCTRL(2:0)
IACCDIF(5:0)
PUSL(3:0)
JTAG_EMULATION(6:0)
AUDUEMCTRL(3:0)
PX_90
R2700
EMIF03-SIM02F2
VSIM
SIM_I_0
SIM_CLK
SIM_RST
"SIMIODAO"
"SIMCLK"
"SIMRST"
SIM Reader Side
SIMIF(3:0)
PX_44
PX_45
PX_46
X2700
470191501
"CLK"
"RST"
"VSIM"
"I/O"
"VSIM"
"GND"
E2700
C2700
100n
C2701
10p
Changed to low C type


TXLB
TXHB
RXHBP
RXHBM
RXLBM
RXLBP
ANT
POWK
ACCDIF(2:0)
SIMIF(3:0)
UIDRV(5:0)
XAUDIO(9:0)
AUDIO(4:0)
SLOWAD(6:0)
RFCONT(5:0)
AUXOUT
PUSL(3:0)
RF
schematic
RFCONT(5:0)
SLOWAD(6:0)
AUXOUT
ANT
RXLBP
RXLBM
RXHBM
RXHBP
TXHB
TXLB
RX_LBM
RX_LBP
RX_HBM
RX_HBP
TX_LB
TX_HB
UEM
schematic
RFCONT(5:0)
SLOWAD(6:0)
AUXOUT
AUDIO(4:0)
XAUDIO(9:0)
UIDRV(5:0)
SIMIF(3:0)
ACCDIF(2:0)
POWK
PUSL(3:0)
AUDUEMCTRL(3:0)
RFCONVCTRL(2:0)
GENIO(31:0)
IACCDIF(5:0)
AUDIODATA(3:0)
RFCONVCLK
UPP
schematic
XANA(6:0)
MEMCONT(9:0)
DSP_MCUTEST(2:0)
KEYB(10:0)
LCDUI(3:0)
MEMADDA(24:0)
JTAG_EMULATION(6:0)
PUSL(3:0)
AUDUEMCTRL(3:0)
RFCONVCTRL(2:0)
GENIO(31:0)
IACCDIF(5:0)
AUDIODATA(3:0)
RFCONVCLK
RX_LBM
RX_LBP
RX_HBM
RX_HBP
TX_LB
TX_HB
TEST_MODE
EXT_26M
XANA(6:0)
MEMCONT(9:0)
DSP_MCUTEST(2:0)
KEYB(10:0)
LCDUI(3:0)
MEMADDA(24:0)
JTAG_EMULATION(6:0)
TEST_MODE
EXT_26M
RFCONVCLK
AUDIODATA(3:0)
IACCDIF(5:0)
GENIO(31:0)
RFCONVCTRL(2:0)
AUDUEMCTRL(3:0)

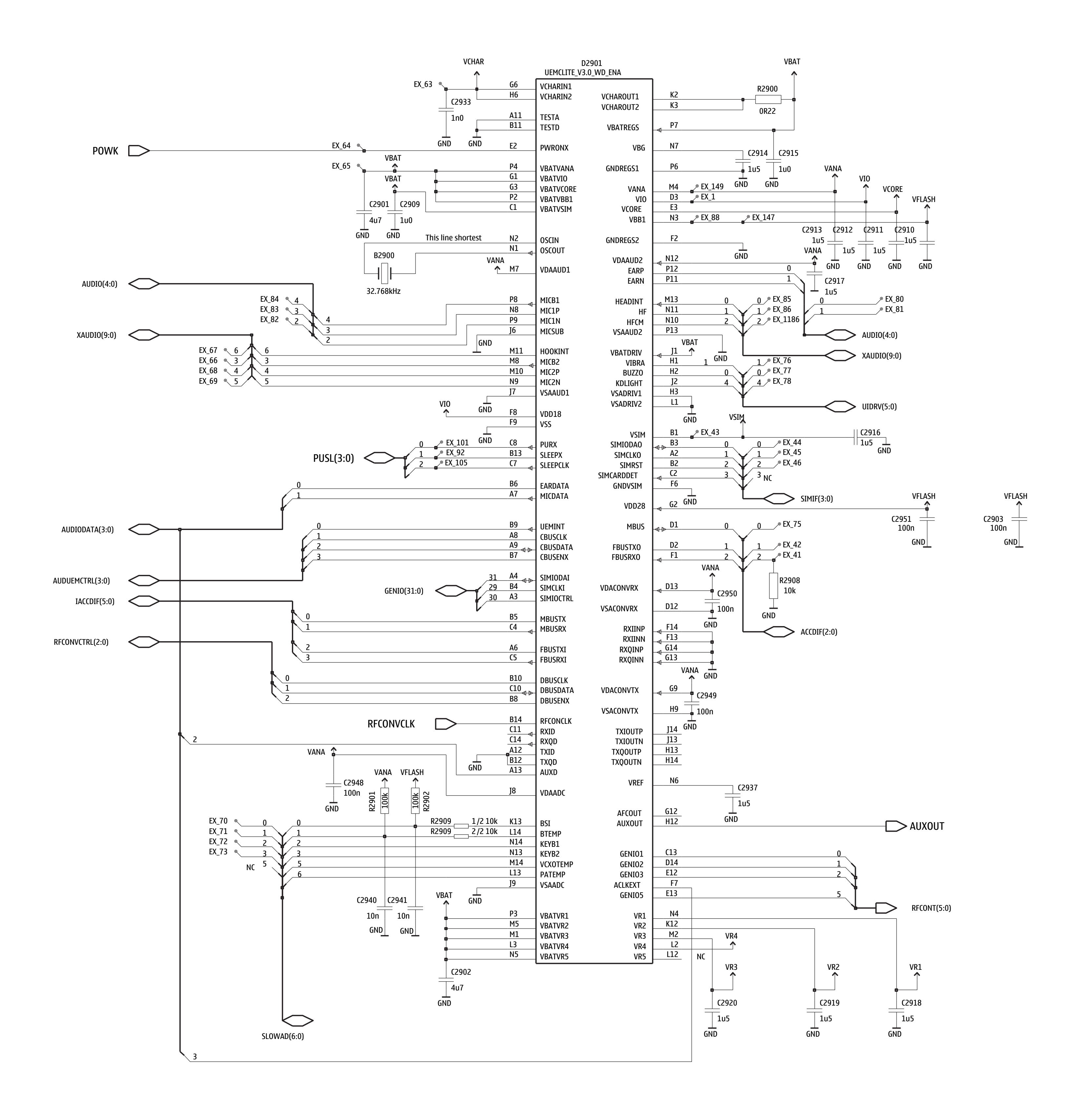

A8000
BB SHIELDING ASSY
040-035774
A8001
FEM
SHIELD
040-035777
GND

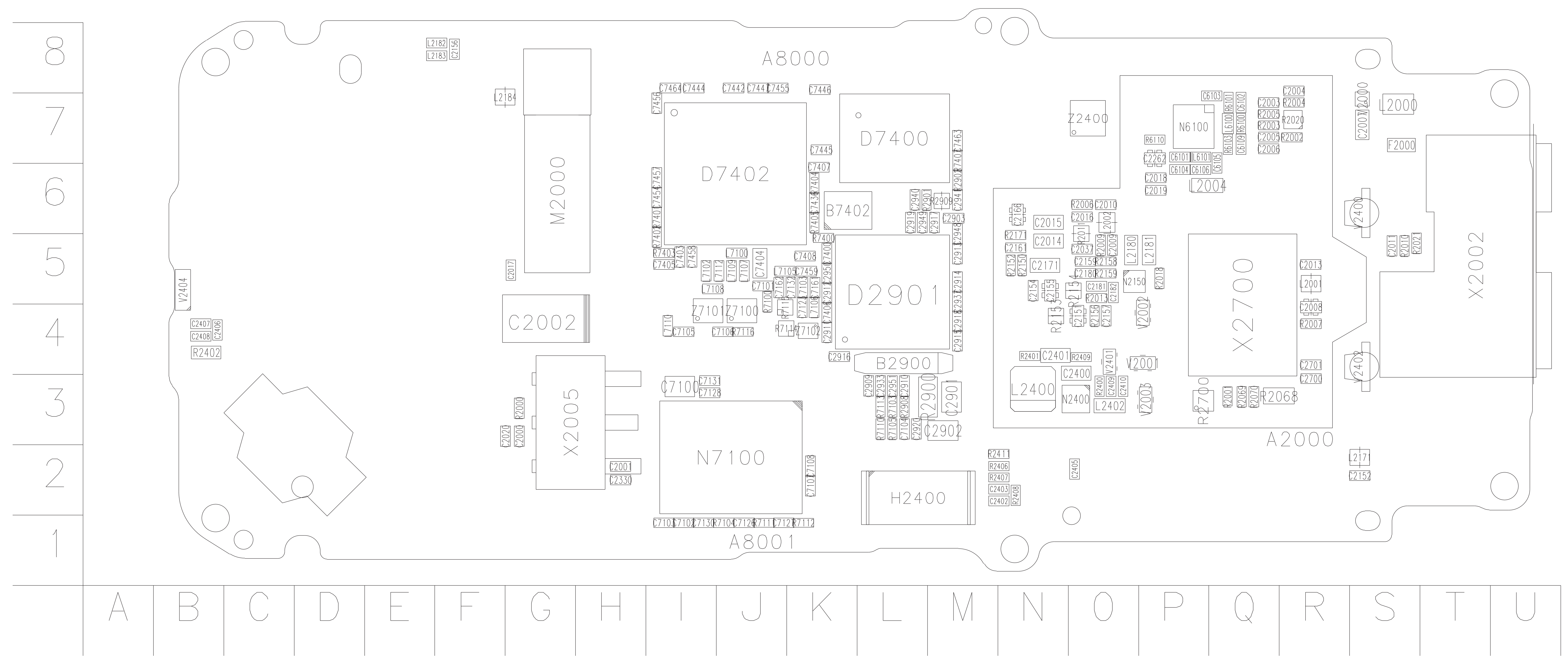

RXLBP
RX_LBP
RX_LBM
RXLBM
not_assembled
L7112
4n7H
L7101
2n2H
L7108
15nH
L7102
4n7H
Z7101
942.5MHz
SAFEK942MFL0F04R13
Out
Out
In
GND
C7105
12p
C7165
0p5
not_assembled
L7110
15nH
N7100
RPF88120B_S_21WS0
TXIN_LB
RXOOUT_LB
ANT
PA_ID
TXIN_HB
RXOUT_HB
OL2
OL1
TXP
VPC
VREG
VBAT_LB
VBAT_HB
VBAT_PC
VC1
VC2
VC3
GND PINS=
ANT
EX_59
VANA
R7113
100k
R7110
2k2
SLOWAD(6:0)
RXHBM
RX_HBM
RX_HBP
RXHBP
L7107
12nH
L7100
15nH
L7113
6n8H
not_assembled
L7109
12nH
Z7100
SAFEK1G84FC0F04R13
1842.5MHz
C7166
1p5
not_assembled
R7116
0R
REL2
C7106
1p5
TXLB
VDDRFIO
L7105
2n2H
C7161
10p
C7132
47p
L7103
5n6H
Z7102
EFCH897MTDB2
897.5MHz
OUT
IN
L7106
3n3H
C7123
6p8
TX_LB
R7114
RESNET_3DB
17R6
292R
292R
R7115
RESNET_6DB
37R4
151R
151R
R7100
0R
C7162
10p
TX_HB
C7101
2p2
REL2
TXHB
R7112
10k
AUXOUT
C7127
120p
VBAT
EX_93
EX_3
R7111
10R
VR3
C7108
5p6
C7107
27p
C7131
1n0
C7128
15p
C7100
2.2u
C7130
27p
C7126
22p
TP7100
TP7101
TP7102
TP7103
RFCONT(5:0)
GND


R7103
VANA
100k
SLOWAD(6:0)
R7105
1k0
R7104
47k
C7103
1n0
C7102
8p2
C7104
33p
GND

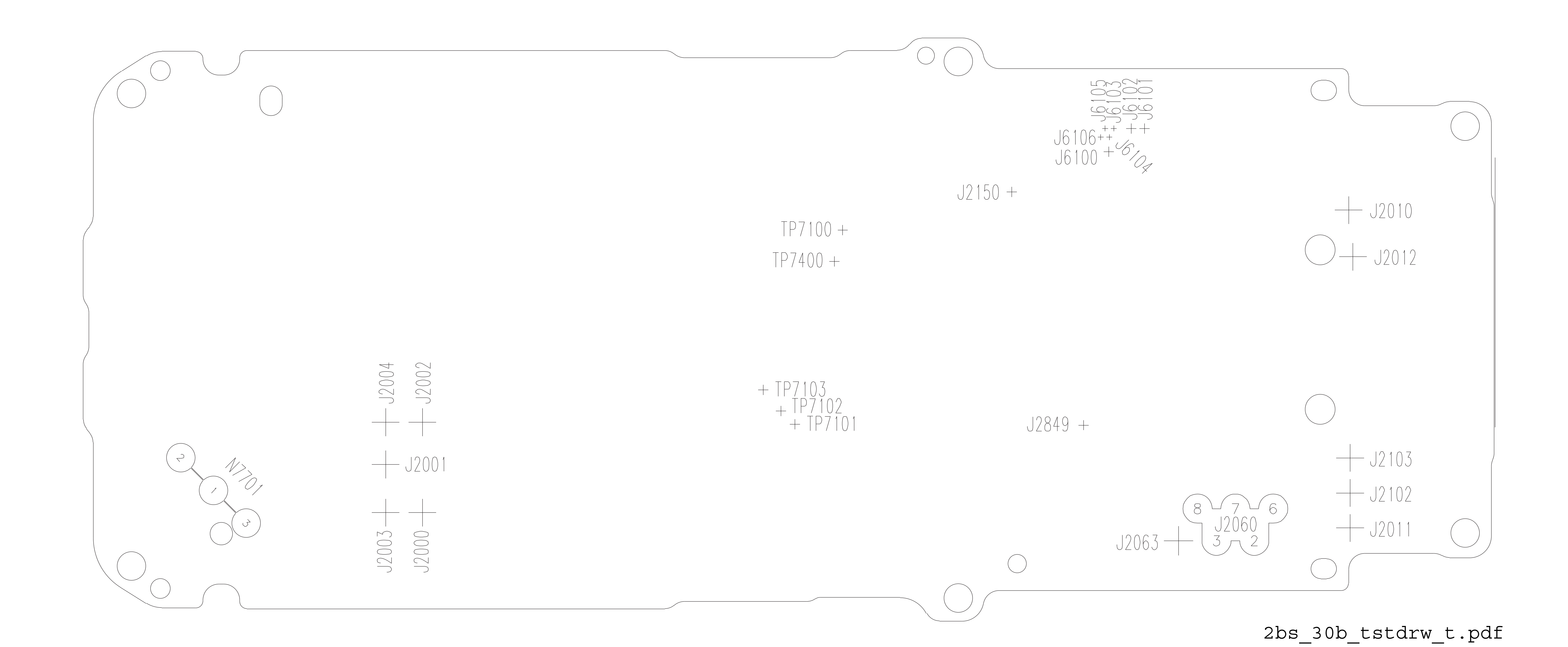


GENIO(31:0)
LCDUI(3:0)
KEYB(10:0)
DSP_MCUTEST(2:0)
D7400
K8S3215ETE-SE7C
NOR FLASH 2MX16
ADDRESS/DATA I/O
D7402
UPPCOSTO_V1.3A_PROD_UMC12A
MEMCONT(9:0)
MEMADDA(24:0)
JTAG_EMULATION(6:0)
PUSL(3:0)
AUDUEMCTRL(3:0)
IACCDIF(5:0)
RFCONVCTRL(2:0)
RFCONVCLK
AUDIODATA(3:0)
EXT_26M
TX_LB
TX_HB
RX_HBM
RX_HBP
RX_LBP
RX_LBM
XANA(6:0)
TEST_MODE
R7407 4k7
R7404 1M0
R7405 100k
R7400 100R
R7401 100R
R7402 100R
R7403 45k3
C7447 10n
C7457 100n
C7463 100n
C7436 10n
C7400 10p
C7464 1u0
C7446 10n
C7445 10n
C7456 100n
C7444 100n
C7455 100n
C7442 2n7
C7454 220p
C7408 1u0
C7406 100n
C7403 0u47
C7407 1u0
C7459 1u0
C7404 4u7
C7405 100n
C7458 1u0
B7402 26MHz
TP7400
VIO
VPP
VCORE
VDDX
VR2
VREF
VR1
VDDRFIO
IREF
VDDOSC
VDDA
GND
NC





2bs_30b_asmdrw_t.pdf



2bs_30b_tstdrw_t.pdf